

**AMENDMENTS TO THE CLAIMS:**

1. (Currently amended) A transmission circuit, comprising:

\_\_\_\_\_ a baseband circuit for generating and outputting ~~at least one~~ transmission data  
~~constituted by including~~ first and second channel ~~data, data;~~

\_\_\_\_\_ a spreading ~~means-unit~~ for spreading the transmission data with a spreading code that  
differs for each transmission ~~channel, channel;~~

\_\_\_\_\_ a multiplication ~~means-unit~~ for respectively weighting amplitudes of the first and  
second channel data by using a combination of two gain factors determined by a transmission  
~~data-rate, rate;~~

\_\_\_\_\_ a digital modulation ~~means-unit~~ for digitally modulating the first and second channel  
data whose amplitudes are weighted by said multiplication ~~means-unit; and~~

\_\_\_\_\_ a quadrature modulator for quadrature-modulating the first and second channel data  
digitally modulated by said digital modulation ~~means-unit~~ and outputting the data as a  
transmission signal, ~~and an antenna for emitting the transmission signal output from said~~  
~~quadrature modulator as a radio wave, wherein;~~

~~wherein~~ said multiplication ~~means-unit~~ weights the amplitudes of the first and second  
channel data by using gain factors that keep power of the transmission signal output from said  
quadrature modulator constant regardless of the transmission data rate without changing a  
ratio of a combination of gain factors determined by the transmission data rate, ~~and~~

\_\_\_\_\_ said multiplication unit weights the amplitudes of the first and second channel data by  
using gain factors determined on the basis of the power of the transmission signal output from  
said quadrature modulator without changing the ratio of the combination of gain factors  
determined by the transmission data rate.

2. (Canceled)

3. (Currently amended) A transmission circuit as claimed in claim 1, wherein said multiplication ~~means-unit~~ weights the amplitudes of the first and second channel data by using gain factors that make a sum of a square of a gain factor for weighting the amplitude of the first channel data and a square of a gain factor for weighting the amplitude of the second channel data constant regardless of the transmission data rate without changing a ~~the~~ ratio of a ~~the~~ combination of gain factors determined by the transmission data rate.

4. (Currently amended) A transmission circuit as claimed in claim 1, wherein said baseband circuit comprises a table storing a gain factor determined by the transmission data rate and a gain factor used by said multiplication ~~means-unit~~ to weight the transmission data, and outputs a gain factor corresponding to the transmission data rate from said table to said multiplication ~~means-unit~~ on the basis of the transmission data rate.

5. (Canceled)

6. (Currently amended) A transmission circuit as claimed in claim 3, wherein said baseband circuit comprises a table storing a gain factor determined by the transmission data rate and a gain factor used by said multiplication ~~means-unit~~ to weight the transmission data, and outputs a gain factor corresponding to the transmission data rate from said table to said multiplication ~~means-unit~~ on the basis of the transmission data rate.

7. (Currently amended) A transmission circuit, comprising:

\_\_\_\_\_ a baseband circuit for generating and outputting ~~at least one~~ transmission data constituted by including first and second channel ~~data, data;~~

\_\_\_\_\_ a spreading ~~means-unit~~ for spreading the transmission data with a spreading code that differs for each transmission ~~channel, channel;~~

\_\_\_\_\_ a multiplication ~~means-unit~~ for respectively weighting amplitudes of the first and second channel data by using a combination of two gain factors determined by a transmission ~~data-rate, rate;~~

\_\_\_\_\_ a digital modulation ~~means-unit~~ for digitally modulating the first and second channel data whose amplitudes are weighted by said multiplication ~~means, unit;~~

\_\_\_\_\_ a quadrature modulator for quadrature-modulating the first and second channel data digitally modulated by said digital modulation ~~means-unit~~ and outputting the data as a transmission ~~signal, signal;~~ and

\_\_\_\_\_ an antenna for emitting the transmission signal output from said quadrature modulator as a radio wave,

wherein said transmission circuit further comprises:

(~~a~~) an amplification ~~means-unit~~ for amplifying the transmission signal output from said quadrature modulator with a gain based on a control voltage;

(~~b~~) a transmission level circuit for determining a transmission power value of the second channel data component;

(~~c~~) a first gain offset circuit for adding, to a transmission power value determined by said transmission level circuit, a first gain correction amount for controlling a gain of said

amplification ~~means-unit~~ to keep transmission power of the second channel data component at the antenna end constant regardless of the transmission data rate by using a combination of two gain factors determined by the transmission data rate, and outputting the transmission power value; and

(d) a voltage generating circuit for generating a voltage for controlling the gain of said amplification ~~means-unit~~, on the basis of the transmission power value output from said first gain offset circuit, and

wherein said antenna emits the transmission signal output from said quadrature modulator and amplified by said amplification ~~means-unit~~ as a transmission signal.

8. (Currently amended) A transmission circuit as claimed in claim-4 31, wherein:  
said baseband circuit comprises a table storing a gain factor determined by the transmission data rate and a gain factor used by said multiplication unit to weight the transmission data, and outputs a gain factor corresponding to the transmission data rate from said table to said multiplication unit on the basis of the transmission data rate, and  
said transmission circuit further comprises:

(a) an amplification ~~means-unit~~ for amplifying the transmission signal output from said quadrature modulator with a gain based on a control voltage;

(b) a transmission level circuit for determining a transmission power value of the second channel data component;

(c) a first gain offset circuit for adding, to a transmission power value determined by said transmission level circuit, a first gain correction amount for controlling a gain of said amplification ~~means-unit~~ to keep transmission power of the second channel data component

at the antenna end constant regardless of the transmission data rate by using a combination of two gain factors determined by the transmission data rate, and outputting the transmission power value; and

(d) a voltage generating circuit for generating a voltage for controlling the gain of said amplification ~~means~~ unit, on the basis of the transmission power value output from said first gain offset circuit, and

wherein said antenna emits the transmission signal output from said quadrature modulator and amplified by said amplification ~~means~~ unit as a transmission signal.

9. (Canceled)

10. (Currently amended) A transmission circuit as claimed in claim ~~6~~ 31, wherein:  
said multiplication unit weights the amplitudes of the first and second channel data by using gain factors that make a sum of a square of a gain factor for weighting the amplitude of the first channel data and a square of a gain factor for weighting the amplitude of the second channel data constant regardless of the transmission data rate without changing the ratio of the combination of gain factors determined by the transmission data rate,

said baseband circuit comprises a table storing a gain factor determined by the transmission data rate and a gain factor used by said multiplication unit to weight the transmission data, and outputs a gain factor corresponding to the transmission data rate from said table to said multiplication unit on the basis of the transmission data rate, and

said transmission circuit further comprises:

(a) an amplification ~~means~~ unit for amplifying the transmission signal output from

said quadrature modulator with a gain based on a control voltage;

(b) a transmission level circuit for determining a transmission power value of the second channel data component;

(c) a first gain offset circuit for adding, to a transmission power value determined by said transmission level circuit, a first gain correction amount for controlling a gain of said amplification ~~means-unit~~ to keep transmission power of the second channel data component at the ~~amplitude-antenna~~ end constant regardless of the transmission data rate by using a combination of two gain factors determined by the transmission data rate, and outputting the transmission power value; and

(d) a voltage generating circuit for generating a voltage for controlling the gain of said amplification ~~means-unit~~ on the basis of the transmission power value output from said first gain offset circuit, and

wherein said antenna emits the transmission signal output from said quadrature modulator and amplified by said amplification ~~means-unit~~ as a transmission signal.

11. (Original) A transmission circuit as claimed in claim 7, wherein said first gain offset circuit calculates transmission power of the first channel data component by using a combination of two gain factors determined by the transmission data rate, adds the transmission power as the first gain correction amount to the transmission power value determined by said transmission level circuit, and outputs the transmission power value.

12. (Original) A transmission circuit as claimed in claim 8, wherein said first gain offset circuit calculates transmission power of the first channel data component by using a

combination of two gain factors determined by the transmission data rate, adds the transmission power as the first gain correction amount to the transmission power value determined by said transmission level circuit, and outputs the transmission power value.

13. (Canceled)

14. (Original) A transmission circuit as claimed in claim 10, wherein said first gain offset circuit calculates transmission power of the first channel data component by using a combination of two gain factors determined by the transmission data rate, adds the transmission power as the first gain correction amount to the transmission power value determined by said transmission level circuit, and outputs the transmission power value.

15. (Currently amended) A transmission circuit as claimed in claim 7, further comprising a second gain offset circuit for adding, to the transmission power value output from said first gain offset circuit, a second gain correction amount which is used to correct an output power error caused in said quadrature modulator when said multiplication ~~means~~unit weights the amplitudes of the first and second channel data by using gain factors for weighting the amplitudes,

wherein said voltage generating circuit generates a voltage for controlling the gain of said amplification~~means~~unit, on the basis of the transmission power value output from said second gain offset circuit.

16. (Currently amended) A transmission circuit as claimed in claim 8, further

comprising a second gain offset circuit for adding, to the transmission power value output from said first gain offset circuit, a second gain correction amount which is used to correct an output power error caused in said quadrature modulator when said multiplication ~~means~~ unit weights the amplitudes of the first and second channel data by using gain factors for weighting the amplitudes,

wherein said voltage generating circuit generates a voltage for controlling the gain of said amplification-~~means~~ unit, on the basis of the transmission power value output from said second gain offset circuit.

17. (Canceled)

18. (Currently amended) A transmission circuit as claimed in claim 10, further comprising a second gain offset circuit for adding, to the transmission power value output from said first gain offset circuit, a second gain correction amount which is used to correct an output power error caused in said quadrature modulator when said multiplication ~~means~~ unit weights the amplitudes of the first and second channel data by using gain factors for weighting the amplitudes,

wherein said voltage generating circuit generates a voltage for controlling the gain of said amplification-~~means~~ unit, on the basis of the transmission power value output from said second gain offset circuit.

19. (Currently amended) A transmission circuit as claimed in claim 15, wherein said second gain offset circuit calculates a ratio between output power of said quadrature



modulator set when one combination of gain factors of gain factors used to weight the amplitudes of the first and second channel data by said multiplication ~~means-unit~~ is set as a reference combination, and the reference combination of gain factors are used, and output power of said quadrature modulator set when gain factors used to weight the amplitudes of the first and second channel data by said multiplication ~~means-unit~~ are used, adds the ratio as the second gain correction amount to the transmission power output from said first gain offset circuit, and outputs the transmission power.

20. (Currently amended) A transmission circuit as claimed in claim 16 wherein said second gain offset circuit calculates a ratio between output power of said quadrature modulator set when one combination of gain factors of gain factors used to weight the amplitudes of the first and second channel data by said multiplication ~~means-unit~~ is set as a reference combination, and the reference combination of gain factors are used, and output power of said quadrature modulator set when gain factors used to weight the amplitudes of the first and second channel data by said multiplication ~~means-unit~~ are used, adds the ratio as the second gain correction amount to the transmission power output from said first gain offset circuit, and outputs the transmission power.

21. (Canceled)

22. (Currently amended) A transmission circuit as claimed in claim 18 wherein said second gain offset circuit calculates a ratio between output power of said quadrature modulator set when one combination of gain factors of gain factors used to weight the

amplitudes of the first and second channel data by said multiplication ~~means-unit~~ is set as a reference combination, and the reference combination of gain factors are used, and output power of said quadrature modulator set when gain factors used to weight the amplitudes of the first and second channel data by said multiplication ~~means-unit~~ are used, adds the ratio as the second gain correction amount to the transmission power output from said first gain offset circuit, and outputs the transmission power.

23. (Currently amended) A transmission circuit as claimed in claim 19, wherein said second gain offset circuit includes a table storing a gain factor determined by the transmission data rate and a gain factor used by said multiplication ~~means-unit~~ to weight the transmission data.

24. (Currently amended) A transmission circuit as claimed in claim 20, wherein said second gain offset circuit includes a table storing a gain factor determined by the transmission data rate and a gain factor used by said multiplication ~~means-unit~~ to weight the transmission data.

25. (Canceled)

26. (Currently amended) A transmission circuit as claimed in claim 22, wherein said second gain offset circuit includes a table storing a gain factor determined by the transmission data rate and a gain factor used by said multiplication ~~means-unit~~ to weight the transmission data.

27. (Original) A transmission circuit as claimed in claim 1, wherein the first channel data is data channel data of the transmission data, and  
the second channel data is control channel data of the transmission data.

28. (Original) A transmission circuit as claimed in claim 7, wherein the first channel data is data channel data of the transmission data, and  
the second channel data is control channel data of the transmission data.

29. (Currently amended) A transmission circuit as claimed in claim 1, wherein said digital modulation ~~means-is-unit~~ comprises a phase modulation means-unit for phase shifting modulating amplitude data of the first and second channel data whose amplitudes are weighted by said multiplication-~~means~~ unit.

30. (Currently amended) A transmission circuit as claimed in claim 7, wherein said digital modulation ~~means-is-unit~~ comprises a phase modulation means-unit for phase shifting modulating amplitude data of the first and second channel data whose amplitudes are weighted by said multiplication-~~means~~ unit.

31. (New) A transmission circuit as claimed in claim 1, further comprising an antenna for emitting the transmission signal output from said quadrature modulator as a radio wave.